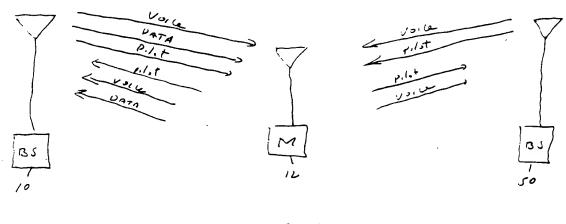
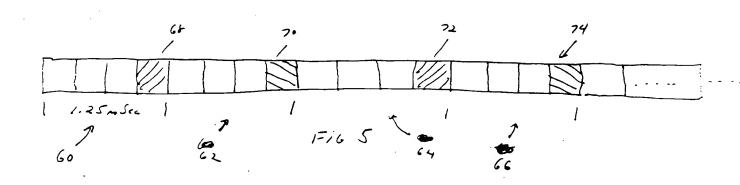
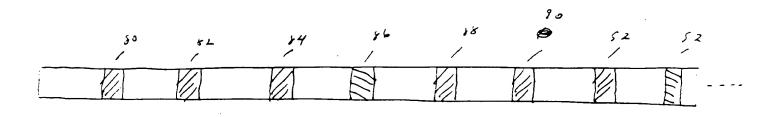


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	CONFIGURATION NA	ME	Power control Bit Pattern (Cramp			
	V50D750		∨ DDD		DDDD	DDDD
_	V100⊅700		∨ DDD	CAGE		DDDD
	V 1503650					
	V 200 D600		VDDD	VDDD	∨20DD	<b>م</b> رور ۲
·	V 250 ⊅550					_ vamb
YOLCE	V 300 D500		VDVD	V⊅⊅D	<b>√⊅</b> ∨ <b>⊅</b>	\DDD ——————————————————————————————————
dala	V 350 D450			<u> </u>	<u> </u>	_עמתי
	V 400 ⊅ 400		√J\\D	VDVD	VDVD	V <b>⊅</b> VJD
	1				<u> </u>	VDVD_
	:					
	V 760 D50		VDVV	VDVV	VDVV	V20V V
երով։ Գրով։ Վորի Կուս - իչ		FIG 7		-		

ŧ	TIME ->				
	Mubile Received Data Burst	MOBILE PROCESS DATA BITS AND ASSIGNED WALLY CODE	A FIRST REVEOSÉ LINK FRAME BOUNDRY		

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